



XA-9629
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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DEC 05 2003

TC 1700

In re the application of:

Yutaka SHIMADA et al.

Appln. No.: 10/083,402

Group Art Unit: 1746

Filed: February 27, 2002

Examiner: Z. El Arini

For: MANUFACTURING METHOD OF SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICE

* * *

AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed July 23, 2003,
please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing
of Claims which begins on page 2 of this paper.

Remarks begin on page 22 of this paper.